

Name: _____ Registration #: _____ Section: _____

Applications of ICT

Session 2025 Spring

Midterm Exam

Time: 60 minutes

Maximum Marks = 30

Q-1: Draw a Block Diagram identifying Main Basic Parts of a Microprocessor Connection with RAM, ROM and Peripheral Devices **(4)**

Q-2: What does Fetch-Decode-Execute mean? Explain this Process in Detail with Block Diagram **(2+4)**

Q-3: What is the difference between RAM and ROM? **(3)**

Q-4: Convert a RAM Memory Location address **0 x 456AB9** into its Binary Equivalent ? Is the RAM of a 32bit computer or 64 bit computer ?**(3)**

Q-5: Explain the Usage of Big Data with respect to Three main Attributes **(4)**

Q-6: Write down any Present day 5 search engines available on the Web? **(2)**

Q-7: Explain the Difference between a LAN and WAN with an example ? **(3)**

Q-8: What does HTML Stand for? How a Server communicates with a Browser over World Wide Web **(1+3)**